



Date : 23/12/2013

NOTICE

All the students of SE /TE/BE are hereby informed that Bridge course is scheduled from 26/12/2013 to 01/01 2014 as per the time 9 am to 4:40 pm displayed on notice board . Follow the given schedule and be present for the same . Attendance is compulsory.

HOD

(Mrs. Leena Govekar)

H. O. D.

Electronics Dept.

Padmabhanu
College of Engineering
Mumbai - 400 022

BRIDGE TIME TABLE

TITLE : ACADEMIC SCHEDULE (FIRST HALF – 2014)	
DEPT : ELECTRONICS ENGINEERING	
w.e.f. : 26/12/13 TO 01/01/2014	CLASS:-BE- VIII- SEM
CLASS ROOM NO-321	

TIME(HRS) DAYS	9.00-10.00 hrs	10.00 –11.00 hrs	11.00-12.00 hrs	12.00 – 31.00 hrs	13.00-13.30	13.30-14.30 hrs	14.30-15.30 hrs	15.30-16.30 hrs
Monday	AVLSI (ST)	R & A (SND)	ESRTP (PT)	EPD(RP)-321 ANT(SB)-005 NNFL-(NZ)-320	L	R & A (SND)	AVLSI (ST)	ESRTP (PT)
Tuesday	AVLSI (ST)	R & A (SND)	ESRTP (PT)	EPD(RP)-321 ANT(SB)-005 NNFL-(NZ)-320	U	R & A (SND)	ESRTP (PT)	ESRTP (PT)
Wednesday	R & A (SND)	EPD(RP)-321 ANT(SB)-005 NNFL-(NZ)-320	AVLSI (ST)	ESRTP (PT)	N	EPD(RP)-321 ANT(SB)-005 NNFL-(NZ)-320	R & A (SND)	AVLSI (ST)
Thursday	AVLSI (ST)	AVLSI (ST)	R & A (SND)	R & A (SND)	C	EPD(RP)-321 ANT(SB)-005 NNFL-(NZ)-320	EPD(RP)-321 ANT(SB)-005 NNFL-(NZ)-320	ESRTP (PT)
Friday	EPD(RP)-321 ANT(SB)-005 NNFL-(NZ)-320	AVLSI (ST)	R & A (SND)	ESRTP (PT)	H	EPD(RP)-321 ANT(SB)-005 NNFL-(NZ)-320	AVLSI (ST)	ESRTP (PT)

SUB	Class/Lab Teacher	Theory	TOTAL
AVLSI - Advance VLSI Design	Mrs. Shilpa Temkar(ST)	09	09
ES & RTP – Embedded Systems and Real-Time Programming	Mrs. Priti Tyagi(PT)	09	09
R & A - Robotics and Automation	Mrs. S N Deshpande(SND)	09	09
EPD - Electronics Product Design	Mr. Ramakant Patil(RP)	08	08
NNFS – Neural Networks & Fuzzy Systems	Mrs. Nilima Zade (NZ)	08	08
ANT-Advance Network Technology	Mrs. Suvarna Bise(SB)	08	08

Medha

Mrs. Medha Kulkarni
(Academic Committee Convener)

Leena

Mrs. Leena Govekar
(HOD ELEX)

Coor

Dr. K.T.V. Reddy
(PRINCIPAL)

Priya

Mrs. Priya Gupta
(Academic Committee Member)

Radha

Mrs. Radha Wanode
(Academic Committee Member)

BRIDGE TIME TABLE

TE - FH-14

TITLE : ACADEMIC SCHEDULE (FIRST HALF - 2014)

DEPT : ELECTRONICS ENGINEERING

w.e.f. 26/12/13 TO 01/01/2014

CLASS:-TE- VI- SEM

CLASS ROOM NO-320

TIME(HRS) DAYS	9.00-10.00 hrs	10.00-11.00 hrs	11.00-12.00 hrs	12.00-13.00 hrs	13.00-13.30	13.30-14.30hrs	14.30-15.30hrs	15.30-16.30hrs	16.30-17.30 hrs
Monday	MDC (PB)	DTSS (LG)	PE (JP)	EIS (RP)	L	PE (JP)	MPMC-II (PG)	MDC (PB)	EIS (RP)
Tuesday	PE (JP)	EIS (RP)	DTSS (LG)	MPMC-II (PG)	U	MDC (PB)	DTSS (LG)	CO (RW)	PE (JP)
Wednesday	MPMC-II (PG)	CO (RW)	PE (JP)	MPMC-II (PG)	N	EIS (RP)	MDC (PB)	DTSS (LG)	PE (JP)
Thursday	EIS (RP)	MPMC-II (PG)	DTSS (LG)	DTSS (LG)	C	CO (RW)	MDC (PB)	MDC (PB)	PE (JP)
Friday	CO (RW)	DTSS (LG)	MPMC-II (PG)	EIS (RP)	H	PE (JP)	MPMC-II (PG)	EIS (RP)	MDC (PB)

SUB	Class/Lab Teacher	Theory	TOTAL
MDC - Microwave devices and circuits	Mr. Pramod Bhavarathe(PB)	07	07
DTSS - Discrete time Signal and System	Mrs. Leena Govekar(LG)	07	07
EIS - Electronic Instrumentation System	Mr.R.S.Patil(RP)	07	07
MPMC-II - Microprocessor and Microcontroller - II	Mrs. Priya Gupta(PG)	07	07
PE - Power Electronics	Mrs. Jayshree Pawar (JP)	07	07
COA - Computer Organization	Mrs. Radha Wanode (RW)	07	07
TOTAL			35

[Signature]

Mrs. Priya Gupta / Mrs. Radha Wanode
(Academic Committee Member)

[Signature]

Mrs. Medha Kulkarni
(Academic Committee Convener)

[Signature]

Mrs. Leena Govekar
(HOD ELEX)

[Signature]

Dr. K.T.V. Reddy
(PRINCIPAL)

BRIDGE TIME TABLE

SE - FH.14

TITLE : ACADEMIC SCHEDULE (FIRST HALF - 2014)

DEPT : ELECTRONICS ENGINEERING


w.e.f. : 26/12/13 TO 01/01/2014

CLASS:-SE-IV-SEM

CLASS ROOM NO-319

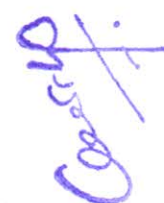
TIME(HRS) DAYS	9.00-10.00 hrs	10.00-11.00 hrs	11.00-12.00 hrs	12.00-1.00 hrs	13.00-13.30 hrs	13.30-14.30 hrs	14.30-15.30 hrs	15.30-16.30 hrs
Monday	EM (NZ)	DEC (RM)	MPP (PG)	FCE (PB)	L	AM-IV (AK)	EM (NZ)	PCS (HJ)
Tuesday	MPP (PG)	PCS (HJ)	PCS (HJ)	AM-IV (AK)	U	DEC (RM)	MPP (PG)	FCE (PB)
Wednesday	MPP (PG)	PCS (HJ)	PCS (HJ)	FCE (PB)	N	DEC (RM)	DEC (RM)	AM-IV (AK)
Thursday	PCS (HJ)	FCE (PB)	AM-IV (AK)	EM (NZ)	C	DEC (RM)	FCE (PB)	AM-IV (AK)
Friday	MPP (PG)	DEC (RM)	AM-IV (AK)	FCE (PB)	H	EM (NZ)	EM (NZ)	PCS (HJ)

SUB	Class/Lab Teacher	Theory	TOTAL
Applied Mathematics - IV	Mrs. Aarti kurkure(AK)	6	6
Discrete Electronic Circuit	Mr. Rohan Mandhare(RM)	6	6
Microprocessor and Peripherals	Mrs. Priya Gupta(PG)	6	6
Principle of Control Systems	Mr. Hemant Jadhav(HJ)	6	6
Fundamentals of Communication Engineering	Mr. Pramod Bhavarthe(PB)	6	6
Electrical Machines	Mrs. Nilima Zade(NZ)	5	5
TOTAL			35


Mrs. Priya Gupta
(Academic Committee Member)

Mrs. Medha Kulkarni
(Academic Committee Convener)


Mrs. Leena Govekar
(HOD ELEX)



Dr. K.T.V. Reddy
(PRINCIPAL)